



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Rohrbaugh et al.

Serial No.: 09/589,338

Filed: June 7, 2000

For: **A Test Pattern Generator Having  
Improved Test Sequence Compaction**

Group Art Unit:

Examiner: Unassigned

Docket No. 10003687-1

Declaration of John Rohrbaugh

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

I, John Rohrbaugh hereby declare that:

1. I am an engineer with Agilent Technologies (formerly Hewlett-Packard Company).
2. I am one of the co-inventors of the above-identified patent application.
3. I have been advised that, as a co-inventor of the above-identified patent application, I have a responsibility to inform the Patent Office of all things that may be considered "material" to the examination of this application. I have been advised that this includes prior art references, as well as other things that the Patent Office may consider to be important to its examination of this application.
4. In connection with this responsibility, and out of an abundance of caution, I wish to bring to the attention of the Patent Office the following information, for its consideration in connection with this patent application.
5. It is my understanding that in 1987, Hewlett-Packard Company internally used a system for testing integrated circuit (IC) chips that performed vector

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generation and filling. Specifically, as I understand the system, it filled otherwise unused bits in a test vector with a constant value (e.g., all ones or all zeros).

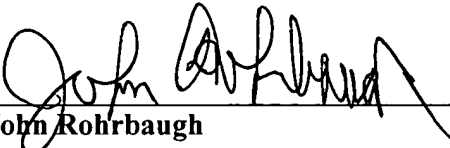
6. It is my further understanding that this system was never disclosed publicly, nor was it embodied in any IC chips sold by Hewlett-Packard Company. That is, it was used only internally during an intermediate step in the IC production.

I hereby declare that all statements made herein are of my own knowledge are true and that all statements are made on information and belief and are believed to be true; and further, that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

7-10-2000

Date

By:

  
John Rohrbaugh